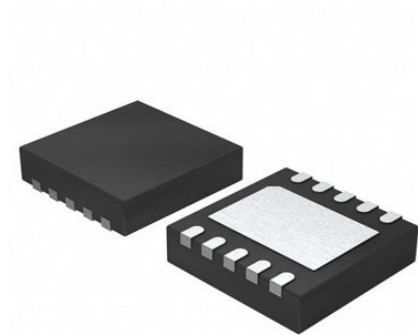


Digital Output Temperature Sensor with On-board SPD EEPROM

Manufacturers	ON Semiconductor, LLC
Package/Case	TDFN-8
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for N34TS04MT3ETG or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The N34TS04 is a combination Temperature Sensor (TS) and 4-Kb of Serial Presence Detect (SPD) EEPROM, which implements the JEDEC TSE2004av DDR4 specification. The TS measures temperature at least 10 times every second. Temperature readings can be retrieved by the host via the serial interface, and are compared to high, low and critical trigger limits stored into internal registers. Over or under limit conditions can be signaled on the open-drain EVENT pin. The 4-Kb memory is partitioned into two individually addressable 2-Kb banks (referred to as SPD pages in the TSE2004av specification). Each bank contains two 1-Kb blocks, which can be Write Protected via Reversible Software Write Protect (RSWP) commands. The SPD EEPROM features a 16-byte wide write buffer and supports the Standard (100 kHz), Fast (400 kHz) and Fast Plus (1 MHz) I2C protocols.

Features

JEDEC TSE2004av Compliant Temperature Sensor

Temperature Range: -20°C to 125°C

DDR4 DIMM Compliant SPD EEPROM

Supply Range: 1.7 V - 5.5 V (SPD EEPROM) and 2.2 V - 5.5 V (TS)

I2C / SMBus Interface

Schmitt Triggers and Noise Suppression Filters on SCL and SDA Inputs

16-Byte Page Write Buffer

Low Power CMOS Technology

2 x 3 x 0.75 mm TDFN Package and 2 x 3 x 0.5 mm UDFN Package

Application

ONSEMI

Related Products



[N01S830HAT221](#)

ON Semiconductor, LLC
TSSOP-8



[CAT24M01YI-GT3](#)

ON Semiconductor, LLC
TSSOP-8



[CAT24M01XI-T2](#)

ON Semiconductor, LLC
SOIC-8



[CAT93C46BVI-GT3](#)

ON Semiconductor, LLC
SOIC-8



[CAT25256YI-GT3](#)

ON Semiconductor, LLC
TSSOP-8



[CAT24C512YI-GT3](#)

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[CAT24C512XI-T2](#)

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SOIC-8



[CAT25512VI-GT3](#)

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