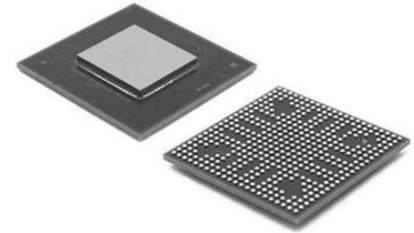


3 Low Noise LDO Voltage Regulator, 200mA Adjustable, 0.5 → 5 V, 0.51V 16-Pin, LFCSP

Manufacturers	Analog Devices, Inc
Package/Case	LFCSP-16
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADP323ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADP322/ADP323 200 mA triple output LDOs combine high PSRR, low noise, low quiescent current, and low dropout voltage to extend the battery life of portable devices and are ideally suited for wireless applications with demanding performance and board space requirements.

The ADP322/ADP323 PSRR is greater than 60 dB for frequencies as high as 100 kHz while operating with a low headroom voltage. The ADP322/ADP323 offer much lower noise performance than competing LDOs without the need for a noise bypass capacitor.

The ADP322/ADP323 are available in a miniature 16-lead, 3 mm × 3 mm LFCSP package and are stable with tiny 1 μF ±30% ceramic output capacitors providing the smallest possible board area for a wide variety of portable power needs.

The ADP322 is available in output voltage combinations ranging from 0.8 V to 3.3 V and offers overcurrent and thermal protection to prevent damage in adverse conditions. The ADP323 adjustable triple LDO can be configured for any output voltage between 0.5 V and 5 V with two resistors for each output.

Features

Fixed (ADP322) and adjustable output (ADP323) options

Bias voltage range (VBIAS): 2.5 V to 5.5 V

LDO input voltage range (VIN1/VIN2, VIN3): 1.8 V to 5.5 V

Three 200 mA low dropout voltage regulators (LDOs)

16-lead, 3 mm × 3 mm LFCSP

Initial accuracy: ±1%

Stable with 1 μF ceramic output capacitors

No noise bypass capacitor required

3 independent logic controlled enables

Overcurrent and thermal protection

Key specifications

High PSRR

76 dB PSRR up to 1 kHz

70 dB PSRR at 10 kHz

60 dB PSRR at 100 kHz

40 dB PSRR at 1 MHz

Low output noise

24 μV rms typical output noise at>

43 μV rms typical output noise at>

Excellent transient response

Low dropout voltage: 110 mV at 200 mA load

85 μA typical ground current at no load, all LDOs enabled

100 μs fast turn on circuit

Guaranteed 200 mA output current per regulator

Application

Mobile phones

Digital cameras and audio devices

Portable and battery-powered equipment

Portable medical devices

Post dc-to-dc regulation

Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc
MSOP-8



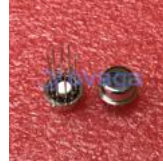
[AD737JRZ](#)

Analog Devices, Inc
SOP-8



[ADP3367ARZ](#)

Analog Devices, Inc
SOIC-8



[AD636JH](#)

Analog Devices, Inc
TO-100-10



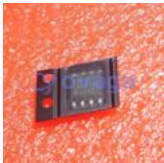
[ADP3330ARTZ3.3-RL7](#)

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[ADR434BRZ](#)

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[ADR421ARZ](#)

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SOP-8



[ADR3412ARJZ-R7](#)

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