

32K X 8 EEPROM,CMOS,HIGH SPEED,PDIP,I.TEMP,120NS,PB FREE HT SUSA
CODE:8542320050

Manufacturers	Renesas Technology Corp
Package/Case	32K X 8 EEPROMCMOSHIGH SPEEDPDIPI.TEMP120NSP
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for X28HC256PIZ-12 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The X28HC256 is a second generation high performance CMOS 32k x 8 EEPROM. It is fabricated with Intersil's proprietary, textured poly floating gate technology, providing a highly reliable 5V only nonvolatile memory. The X28HC256 supports a 128-byte page write operation, effectively providing a 24 μ s/byte write cycle, and enabling the entire memory to be typically rewritten in less than 0.8s. The X28HC256 also features DATA polling and Toggle bit polling, two methods of providing early end of write detection. The X28HC256 also supports the JEDEC standard software data protection feature for protecting against inadvertent writes during power-up and power-down. Endurance for the X28HC256 is specified as a minimum 100,000 write cycles per byte and an inherent data retention of 100 years

Features

Access time: 90ns

Simple byte and page write

Single 5V supply

No external high voltages or $V_{p,p}$ control circuits

Self timed

No erase before write

No complex programming algorithms

No overerase problem

Low power CMOS

Active: 60mA

Standby: 500 μ A

Software data protection

Protects data against system level inadvertent writes

High-Speed page write capability

Highly reliable Direct Write™ cell

Endurance: 100,000 cycles

Data retention: 100 years

Early end of write detection

DATA polling

Toggle bit polling

RoHS compliant

Related Products



[X28HC256JI-12](#)

Renesas Technology Corp
0037+X



[X28HC64SIZ-70](#)

Renesas Technology Corp
SOIC28



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SOP-28



[X28HC256PZ-15](#)

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