

CPLD CoolRunner -II Family 6K Gates 256 Macro Cells 256MHz 0.18um (CMOS)
Technology 1.8V

Manufacturers	AMD Xilinx, Inc
Package/Case	TQFP100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2C256-6VQ100C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

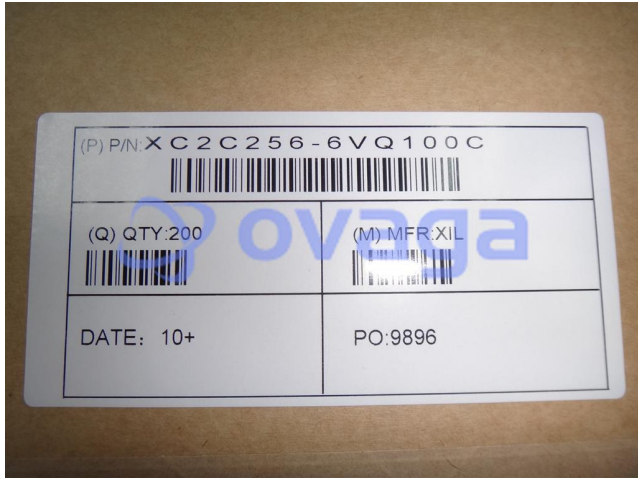
XC2C256-6VQ100C is an FPGA (Field-Programmable Gate Array) device manufactured by Xilinx. Here are some of its features:

Features

- It contains 256,000 system gates
- It has 256 macrocells and 4,608 configurable logic blocks
- It supports a maximum operating frequency of 106.6 MHz
- It has a 6ns input/output delay
- It operates on a 1.8V core voltage and 3.3V I/O voltage

Application

- Communications systems
- Image and video processing
- Aerospace and defense
- Industrial automation
- Medical devices

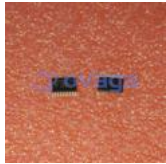


Related Products



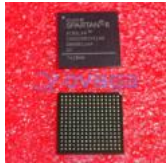
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



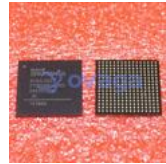
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



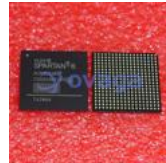
[XCF08PV0G48C](#)

AMD Xilinx, Inc
TSOP-48



[XC6SLX25-3FTG256C](#)

AMD Xilinx, Inc
BGA-256



[XC6SLX16-3CSG324C](#)

AMD Xilinx, Inc
BGA-324



[XCF32PVO48C](#)

AMD Xilinx, Inc
TSOP48