

CPLD XC9500 Family 1.6K Gates 72 Macro Cells 100MHz 0.35um, CMOS Technology 3.3V 100Pin TQFP

Manufacturers	AMD Xilinx, Inc
Package/Case	TQFP100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC9572XL-10TQG100C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

XC9572XL-10TQG100C is a programmable logic device (PLD) manufactured by Xilinx.

Features

- It has 72 macrocells, which can be used as either flip-flops or as combinatorial logic elements.
- It has a maximum of 71 user I/O pins.
- It has a 10ns maximum propagation delay for combinatorial paths.
- It has 1.8V to 5V operation range.
- It has a JTAG boundary-scan testing capability.

Application

- General digital logic design
- Control systems
- Data processing
- Signal processing
- Communications



Related Products



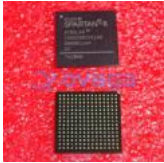
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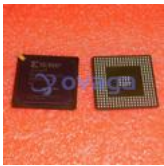
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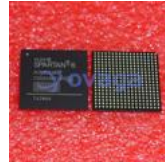
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