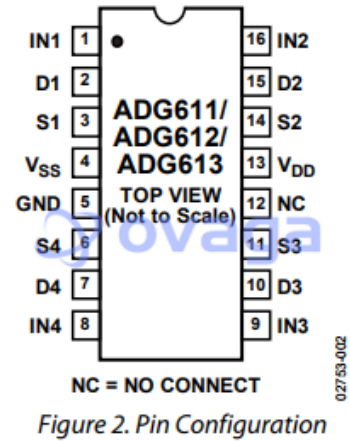


1pC Charge Injection, 100pA Leakage, CMOS ± 5 V/5 V/3 V Quad SPST Switches;
 Package: TSSOP; No of Pins: 16; Temperature Range: Automotive

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-16
Product Type	Analog Switches Multiplexers ; Single Supply 2V to 16V
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG613YRUZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADG613-EP is a monolithic CMOS device containing four independently selectable switches. This switch offers ultralow charge injection of 1 pC over the full input signal range and typical leakage currents of 0.01 nA at 25°C.

The device is fully specified for ± 5 V, 5 V, and 3 V supplies. It contains four independent single-pole, single-throw (SPST) switches. The ADG613-EP contains two switches with digital control logic that turns on with logic low and two switches in which the logic is inverted.

Each switch conducts equally well in both directions when on and has an input signal range that extends to the supplies. The ADG613-EP exhibits break-before-make switching action.

The ADG613-EP is available in a small, 16-lead TSSOP package.

The ADG613-EP is also a TTL-compatible device.

Product Highlights

Ultralow charge injection (1 pC typically).

Dual ± 2.7 V to ± 5.5 V or single 2.7 V to 5.5 V operation.

Temperature range: -55°C to $+125^\circ\text{C}$.

Small, 16-lead TSSOP.

Features

1 pC charge injection

Automotive temperature range: -40°C to $+125^{\circ}\text{C}$

100 pA maximum at 25°C leakage currents

85 Ω on resistance

Rail-to-rail switching operation

Fast switching times

16-lead TSSOP and SOIC packages

Typical power consumption: $<0.1 \mu\text{W}$

TTL-/CMOS-compatible inputs

ADG613-EP supports defense and aerospace applications(AQEC standard)

Download

Military temperature range: -55°C to $+125^{\circ}\text{C}$

Controlled manufacturing baseline

1 assembly/test site

1 fabrication site

Enhanced product change notification

Qualification data available on request

V62/16617 DSCC Drawing Number

Application

Automatic test equipment

Data acquisition systems

Battery-powered systems

Communications systems

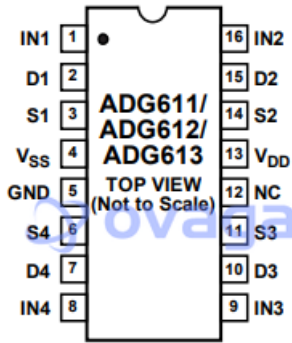
Sample-and-hold systems

Audio signal routing

Relay replacement

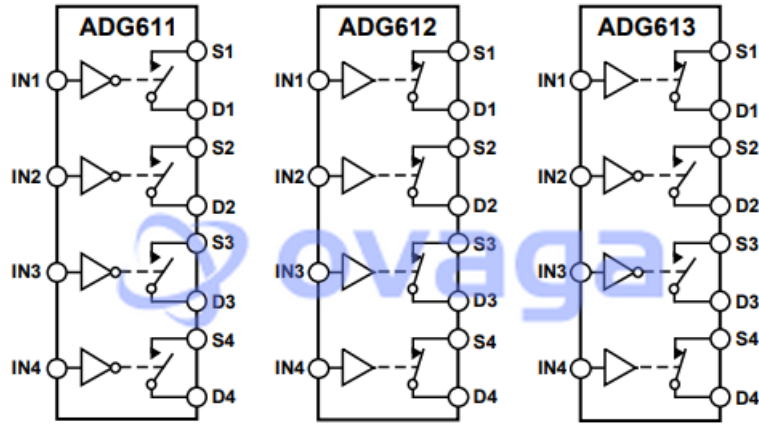
Avionics

FUNCTIONAL BLOCK DIAGRAM



NC = NO CONNECT

Figure 2. Pin Configuration



NOTES

1. SWITCHES SHOWN FOR A LOGIC 1 INPUT.

Figure 1.

02753-001

Related Products



[ADV7181CBSTZ](#)

Analog Devices, Inc
LQFP-64



[AD724JR](#)

Analog Devices, Inc
SOIC-16



[ADV7391WBPCZ](#)

Analog Devices, Inc
LFSCP-3



[ADV7341BSTZ](#)

Analog Devices, Inc
LQFP-64



[AD8170AR](#)

Analog Devices, Inc
SOP8



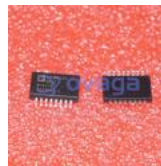
[ADV7393BCPZ](#)

Analog Devices, Inc
LFCSP-VQ-40



[ADV7390BCPZ](#)

Analog Devices, Inc
QFN32



[ADUM4160BRIZ](#)

Analog Devices, Inc
SOIC-16