71V416L10PHG

Data Sheet

SRAM, 4 Mbit, 256K x 16bit, 3V to 3.6V, TSOP, 44 Pins, 10 ns

Manufacturers	Renesas Technology Corp
Package/Case	TSOP-44
Product Type	Memory
RoHS	Rohs
Lifecycle	

Images are for reference only

Please submit RFQ for 71V416L10PHG or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFQ

General Description

The 71V416 3.3V CMOS SRAM is organized as 256K x 16. All bidirectional inputs and outputs of the 71V416 are LVTTL-compatible and operation is from a single 3.3V supply. Fully static asynchronous circuitry is used, requiring no clocks or refresh for operation.

Features

JEDEC Center Power / GND pinout for reduced noise.

Equal access and cycle times

– Commercial and Industrial: 10/12/15ns

One Chip Select plus one Output Enable pin

Bidirectional data inputs and outputs directly

LVTTL-compatible

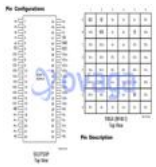
Low power consumption via chip deselect

Upper and Lower Byte Enable Pins

Single 3.3V power supply

Available in 44-pin, 400 mil plastic SOJ, 44-pin, 400 mil TSOP Type II, and 48-pin BGA packages

Related Products



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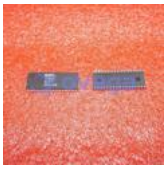
Renesas Technology Corp

SOP44



[71016S12YG]

Renesas Technology Corp

SOJ-44

[7130SA35J]

Renesas Technology Corp

PLCC-52

[71256S70DB]

Renesas Technology Corp

28-CDIP (0.600, 15.24mm)

[71024S20YGI]

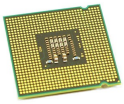
Renesas Technology Corp

SOIC-32

[71321LA55PPGI]

Renesas Technology Corp

52-LQFP



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Renesas Technology Corp

145-LGA

[DF2371VLP34V]

Renesas Technology Corp

145-LGA