

Low Dropout Regulators - LDO 500mA HV LDO 3.3V

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	LFCSP-8
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	

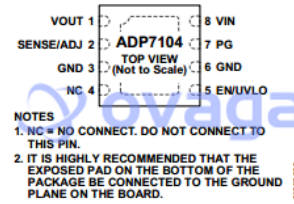


Figure 3. LFCSP Package

Images are for reference only

Please submit RFQ for ADP7104ACPZ-3.3-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADP7104 is a CMOS, low dropout linear regulator that operates from 3.3 V to 20 V and provides up to 500 mA of output current. This high input voltage LDO is ideal for regulation of high performance analog and mixed signal circuits operating from 19 V to 1.22 V rails. Using an advanced proprietary architecture, it provides high power supply rejection, low noise, and achieves excellent line and load transient response with just a small 1  $\mu$ F ceramic output capacitor.

The ADP7104 is available in seven fixed output voltage options and an adjustable version, which allows output voltages that range from 1.22 V to  $V_{IN} - V_{DO}$  via an external feedback divider.

The ADP7104 output noise voltage is 15  $\mu$ V rms and is independent of the output voltage. A digital power-good output allows power system monitors to check the health of the output voltage. A user programmable precision undervoltage lockout function facilitates sequencing of multiple power supplies.

The ADP7104 is available in 8-lead, 3 mm  $\times$  3 mm LFCSP and 8-lead SOIC packages. The LFCSP offers a very compact solution and also provides excellent thermal performance for applications requiring up to 500 mA of output current in a small, low-profile footprint.

## Features

Input voltage range: 3.3 V to 20 V

Maximum output current: 500 mA

Low Noise: 15  $\mu$ V rms for fixed output versions

PSRR Performance of 60 dB at 10 kHz,>

Reverse current protection

Low dropout voltage: 350 mV at 500 mA

Initial accuracy:  $\pm$ 0.8%

Accuracy over line, load, and temperature

Low quiescent current = 900  $\mu$ A with 500 mA load

Low shutdown current: <40  $\mu$ A at>

7 fixed output voltage options: 1.5 V, 1.8 V, 2.5 V, 3 V, 3.3 V, 5 V, and 9 V

Adjustable output from 1.22 V to  $V_{IN} - V_{DO}$

Foldback current limit and thermal overload protection

User programmable precision UVLO/enable

Power-good indicator

8-lead LFCSP and 8-lead SOIC packages

## Application

Regulation to noise sensitive applications: ADC, DAC circuits, precision amplifiers, high frequency oscillators, clocks, and PLLs

Communications and infrastructure

Medical and healthcare

Industrial and instrumentation

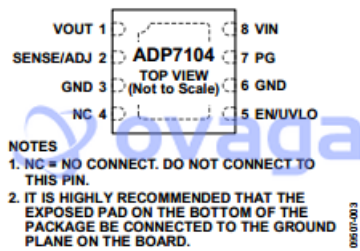
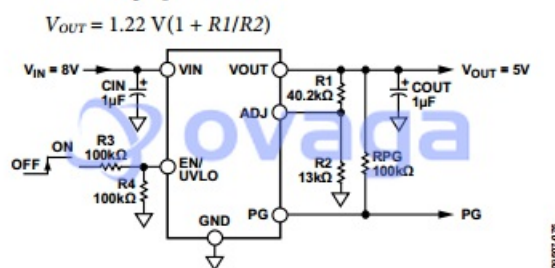


Figure 3. LFCSP Package



## Related Products



[ADP3336ARMZ-REEL7](#)

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MSOP-8



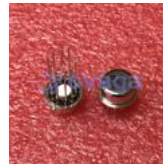
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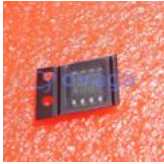
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