

Data Sheet

FT-X3 COMMUNICATIONS TRANFORMER

Manufacturers

Renesas Technology Corp

Package/Case

Product Type

Miscellaneous

Lifecycle

Images are for reference only

Please submit RFQ for 14255R-100 or Final to us: sales@ovaga.com We will contact you in 12 hours.

RFQ

General Description

Features

Integrated FT transceiver

High performance

Four independent cores to manage the physical MAC layer, the communication protocol stacks, the user application, and interrupts

Up to 80MHz system clock

64KB RAM and 16KB ROM

Support for larger external flash memories, up to 256KB applications

Unique 48-bit IEEE MAC ID

Support for up to 254 network variables (NVs), 127 aliases and 254 address table entries

Peripheral interfaces:

Serial interface for external EEPROM or flash

12 I/O pins with 35 programmable standard I/O modes

Hardware USART

Compatible with low-cost surface mount FT-X3 communications transformer

3.3V operation (5V tolerant digital I/O)

Operational temperature range: -40 °C to 85 °C

48pin QFN package (7mm x 7mm)

Built-in LON, LON/IP, BACnet/IP, and BACnet MS/TP stacks provide compatibility with millions of LON and BACnet devices and enabling IP access to every FT 6050 based device

Built-in Free Topology (FT) transceiver provides the most cost-effective, easy-to-install, and easy-to-use interface for communication with twisted-pair cables supporting polarity-insensitive free topology star, daisy chain, bus, loop, or mixed topology wiring and very high noise immunity

A rich set of LonMark and IoT standard profiles and data types further reduce application development time

Easy migration of existing applications written for previous generation Neuron chips and smart transceivers

Free and royalty-free IzoT SDKs for simple integration with other processors

Integrated flash file system for data logging and other applications requiring persistent storage

MikroBUS-compatible FT Click available for FT 6050 evaluation and easy IoT application development