

AD5348BRUZ

Data Sheet

 $2.5~\mathrm{V}$ to $5.5~\mathrm{V},$ Parallel Interface Octal Voltage Output 12-Bit D/A Converter

Manufacturers	Analog Devices, Inc	
Package/Case	TSSOP-38	JSSSSSSSS
Product Type	Data Conversion ICs	JJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJJ
RoHS	Rohs	
Lifecycle		Images are for reference only

Please submit RFQ for AD5348BRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The AD5346/AD5347/AD5348 are octal 8-, 10-, and 12- bit DACs, operating from a 2.5 V to 5.5 V supply. These devices incorporate an onchip output buffer that can drive the output to both supply rails, and also allows a choice of buffered or unbuffered reference input.

The AD5346/AD5347/AD5348 have a parallel interface. CS selects the device and data is loaded into the input registers on the rising edge of WR. A readback feature allows the internal DAC registers to be read back through the digital port.

The GAIN pin on these devices allows the output range to be set at 0 V to VREF or 0 V to 2 x VREF.

Input data to the DACs is double-buffered, allowing simultaneous update of multiple DACs in a system using the LDAC pin.

An asynchronous CLR input is also provided, which resets the contents of the Input Register and the DAC Register to all zeros. These devices also incorporate a power-on-reset circuit that ensures that the DAC output powers on to 0 V and remains there until valid data is written to the device. All three parts are pin-compatible, which allows the user to select the amount of resolution appropriate for their application without redesigning their circuit board.

Features

Low power operation: 1.4 mA (max) at 3.6 V		
Power-down to 120 nA at 3 V, 400 nA at 5 V		
Guaranteed monotonic by design over all codes		
Rail-to-rail output range:0 V to VREF or 0 V to $2 \times \text{VREF}$		
Power-on reset to 0 V		
Simultaneous update of DAC outputs via LDAC pin		
Asynchronous CLR facility		
Readback		
Buffered/unbuffered reference inputs		
20 ns WR time		
38-lead TSSOP/6 mm \times 6 mm 40-lead LFCSP packaging		
Temperature range: -40°C to +105°C		

Application

Portable battery-powered instrumentsDigital gain and offset adjustmentProgrammable voltage and current sourcesOptical networkingAutomatic test equipmentMobile communicationsProgrammable attenuators

Industrial process control

Related Products



ADAS3022BCPZ Analog Devices, Inc LFCSP-40



AD574AJNZ Analog Devices, Inc PDIP-28



AD7938BSUZ Analog Devices, Inc TQFP-32



AD7124-8BCPZ-RL7 Analog Devices, Inc LFCSP-32



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AD7401YRWZ Analog Devices, Inc

AD7266BSUZ

Analog Devices, Inc

SOIC-16
AD7192BRUZ-REEL



AD968 Analog

AD9680BCPZ-500

Analog Devices, Inc LFCSP-64

