

XC3S1000-4FGG320I

Data Sheet

1M Gates 17280 Cells 630MHz 90nm Technology 1.2V

Manufacturers AMD Xilinx, Inc

Package/Case BGA320

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XC3S1000-4FGG320I or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 hours.



General Description

XC3S1000-4FGG320I is a field-programmable gate array (FPGA) manufactured by Xilinx, a leading provider of programmable logic devices. Here are some of its features:

Features

It has 1,008,192 logic cells and 178 user I/Os.

It operates at a maximum frequency of 400 MHz.

It has 36 block RAMs with a total of 648 Kb of memory.

It has 4 Digital Clock Managers (DCMs) that can generate up to 16 different clocks each.

Application

Digital signal processing (DSP)

Image processing

Video processing

Network processing

Industrial automation and control

Aerospace and defense

Medical imaging

Automotive



Related Products



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48