

8 Bit MCU, Low Power High Performance, AVR ATxmega Family ATXmega E Series Microcontrollers, 32 MHz

Manufacturers	<a href="#">Microchip Technology, Inc</a>
Package/Case	VQFN-32
Product Type	Embedded Processors & Controllers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ATXMEGA32E5-MU or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

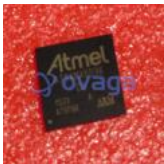
## General Description

The high-performance, low-power 8/16-bit AVR® XMEGA® microcontroller combines 32 KB ISP Flash memory (plus 4 KB boot code section) with read-while-write capabilities, 1 KB EEPROM, 4 KB SRAM, eight-channel event system, a programmable multi-level interrupt controller, 26 general purpose I/O lines, one 16-bit real-time counter, three flexible 16-bit timer/counters with compare modes and PWM, two USARTs (with SPI Master mode), one Two-Wire Interface (TWI) with SMBUs Level 1 support, one Serial Peripheral Interface (SPI), one 16-channel/12-bit 300 ksps A/D converter with optional differential input with programmable gain, one two-channel 12-bit 1 Msps D/A converter, two analog comparators with window mode, a programmable watchdog timer with separate internal oscillator, accurate internal oscillators with PLL and prescaler, and programmable Brown-out Detection.

The XMEGA® E devices feature an innovative XMEGA® Custom Logic module (XCL) consisting of two independent 8-bit timer/counters and two lookup tables used for defining glue logic. It is designed to reduce the bill of material (BOM) and PCB size, as the XCL can replace external circuitry such as delay elements, RS-latches, D-latches, D-flip-flops chip-select logic, AND, NAND, OR, NOR, XOR, XNOR, NOT, MUX AND/OR/XOR logic gates. Also, it can, together with the USART, enable customized communication protocols. By executing powerful instructions in a single clock cycle, the device achieves throughputs approaching one MIPS per MHz, balancing power consumption and processing speed.



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### [ATMEGA32M1-AU](#)

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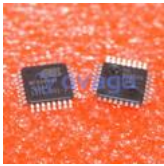
### [ATXMEGA128D3-AU](#)

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