

24LC64 Series 64 Kb (8K X 8) 2.5V Write Protect SER I2C Bus - SOT-23-5

Manufacturers	<a href="#">Microchip Technology, Inc</a>
Package/Case	SOT-23-5
Product Type	Memory
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 24LC64FT-E/OT or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The Microchip Technology Inc. 24LC64F is a 64Kb I2C™ compatible Serial EEPROM. The device is organized as a single block of 8K x 8-bit memory with a 2-wire serial interface. Low-voltage design permits operation down to 2.5V, with standby and read currents of only 1  $\mu$ A and 400  $\mu$ A, respectively. It has been developed for advanced, lowpower applications such as personal communications or data acquisition. The 24LC64F also has a page write capability for up to 32 bytes of data. Functional address lines allow up to eight devices on the same bus, for up to 512 Kbits address space. The 24LC64F is available in the standard 8-pin PDIP, surface mount SOIC, TSSOP, TDFN and MSOP packages. The 24LC64F is also available in the 5-lead SOT-23 package.

## Features

Single-Supply with Operation down to 2.5V

Low-Power CMOS Technology:- Read current 400  $\mu$ A, max.- Standby current 1  $\mu$ A, max. (I-temp)

2-Wire Serial Interface, I2C™ Compatible

Packages with 3 Address Pins are cascadable upto 8 Devices

Schmitt Trigger Inputs for Noise Suppression

Output Slope Control to Eliminate Ground Bounce

100 kHz and 400 kHz Clock Compatibility

Page Write Time 5 ms, typical

Self-timed Erase/Write Cycle

32-Byte Page Write Buffer

Hardware Write-protect for 1/4 Array(1800h-1FFFh)

ESD Protection > 4,000V

More than 1 Million Erase/Write Cycles

Data Retention > 200 Years

Factory Programming Available

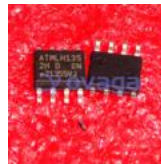


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#### [AT24CM02-SSHM-B](#)

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SOIC-8



#### [AT24CM02-SSHD-B](#)

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