

16-Bit, 75 MIPS, 2.5V, 2 Serial Ports, Host Port, 40 KB RAM; Package: 100 ld LQFP; No of Pins: 100; Temperature Range: Comm

Manufacturers	Analog Devices, Inc
Package/Case	LQFP-100
Product Type	Embedded Processors & Controllers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADSP-2186MKSTZ-300 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADSP-2186M combines the ADSP-2100 family base architecture (three computational units, data address generators and a program sequencer) with two serial ports, a 16-bit internal DMA port, a byte DMA port, a programmable timer, Flag I/O, and extensive interrupt capabilities.

Features

Algebraic assembly language supports C language-style constructs for easy programming

Mini-BGA and 100-lead TQFP package provides for space and power savings (16mm x 16mm)

On-chip RAM and 4 DMA ports make reprogramming easy

16-bit DMA port makes bus interfacing easier

5 sleep and powerdown modes maximize battery life.

13 ns instruction cycle time (75 MIPS)

2.5 volt supply with 3.3 volt I/O

8K words of on-chip program RAM

8K words of on-chip data memory RAM

16-bit internal DMA port for high speed access to on-chip memory

100-lead LQFP and 10mm x 10mm mini-BGA

Application

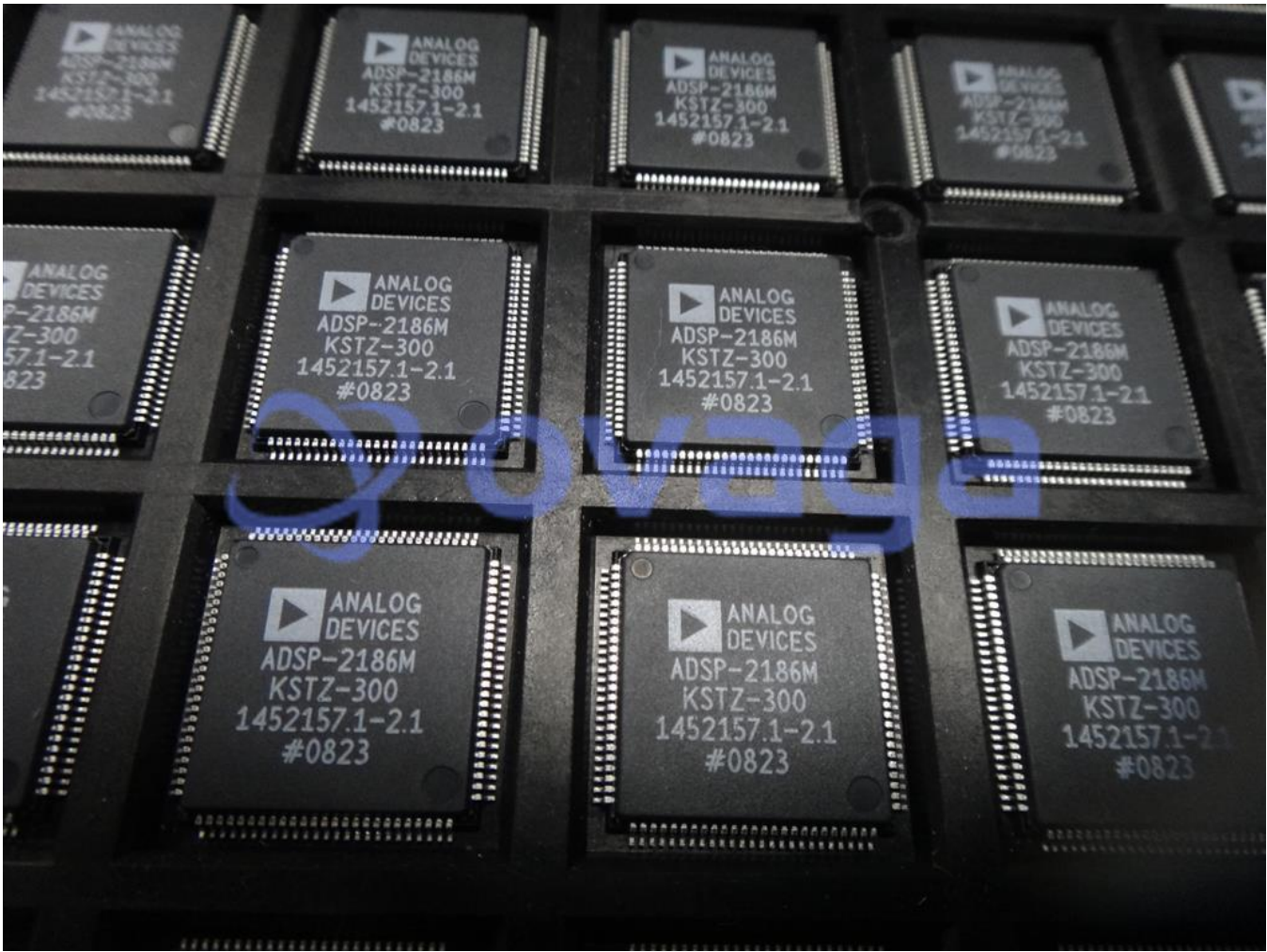
8-bit DMA port to byte memory for transparent program and data memory transfers

Byte memory interface for storage of data tables and program overlays

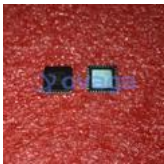
Programmable memory strobe and separate I/O memory space permits "glueless" system

ADSP-2100 Family code and function compatible





Related Products



[ADUC7022BCPZ62](#)

Analog Devices, Inc
LFCSP-40



[ADUC7020BCPZ62](#)

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[ADUC841BSZ62-5](#)

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QFP-52



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[ADSP-BF527BBCZ-5A](#)

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BGA-208



[ADSP-21369BBPZ-2A](#)

Analog Devices, Inc
SBGA-256



[ADSP-BF561SBBCZ-5A](#)

Analog Devices, Inc
CSPBGA-256