

PGA Spartan®-II Family 100K Gates 2700 Cells 263MHz 0.18um Technology 2.5V

| | |
|---------------|---------------------------------|
| Manufacturers | AMD Xilinx, Inc |
| Package/Case | QFP208 |
| Product Type | Programmable Logic ICs |
| RoHS | |
| Lifecycle | |



Images are for reference only

Please submit RFQ for XC2S100-5PQG208I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

XC2S100-5PQG208I is a field-programmable gate array (FPGA) developed by Xilinx, which is a programmable logic device that can be customized by the user after manufacturing. Here are some features of the XC2S100-5PQG208I FPGA:

Features

- It has 100,000 system gates.
- It operates with a maximum frequency of 300 MHz.
- It has 208 pins and comes in a 20 mm x 20 mm package.
- It has 288 user I/Os and can support a variety of different I/O standards.
- It has internal configuration memory that can store up to eight FPGA configurations.

Application

- Digital signal processing (DSP)
- Video and image processing
- Communication systems
- Industrial control systems
- Military and aerospace systems

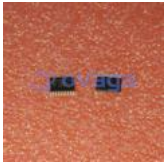


Related Products



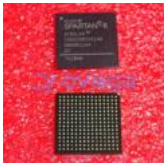
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



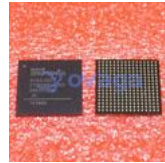
[XCV50-6BG256C](#)

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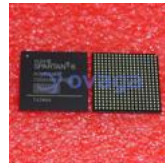
[XCF08PV0G48C](#)

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TSOP-48



[XC6SLX25-3FTG256C](#)

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BGA-256



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