LTC6994IS6-2#TRMPBF

Data Sheet

Programmable Delay Block -40C to 85C Automotive 6-Pin TSOT-23 T/R

Manufacturers	Analog Devices, Inc
Package/Case	SOT-6
Product Type	Clock & Timer ICs
RoHS	Green
Lifecycle	

Images are for reference only

Please submit RFQ for LTC6994IS6-2#TRMPBF or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The LTC6994 is a programmable delay block with a range of 1µs to 33.6 seconds. The LTC6994 is part of the TimerBlox® family of versatile silicon timing devices.

A single resistor, RSET, programs an internal master oscillator frequency, setting the LTC6994's time base. The input-to-output delay is determined by this master oscillator and an internal clock divider, NDIV, programmable to eight settings from 1 to 221.

The output (OUT) follows the input (IN) after delaying the rising and/or falling transitions. The LTC6994-1 will delay the rising or falling edge. The LTC6994-2 will delay both transitions, and adds the option to invert the output.

The LTC6994 also offers the ability to dynamically adjust the delay time via a separate control voltage.

Features

Delay Range: 1µs to 33.6 Seconds

Configured with 1 to 3 Resistors

Delay Max Error:

Delay One or Both Rising/Falling Edges

2.25V to 5.5V Single Supply Operation

70µA Supply Current at 10µs Delay

500µs Start-Up Time

CMOS Output Driver Sources/Sinks 20mA

Available in Low Profile (1mm) SOT-23 (ThinSOT™) and 2mm × 3mm DFN

AEC-Q100 Qualified for Automotive Applications

Application

Noise Discriminators/Pulse Qualifiers

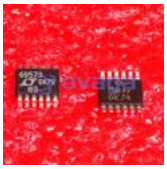
Delay Matching

Switch Debouncing

High Vibration, High Acceleration Environments

Portable and Battery-Powered Equipment

Related Products



[LTC6957HMS-3#PBF]

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MSOP-12

[LTC1799CS5#TRMPBF]

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TSOT23

[LTC6902IMS#PBF]

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MSOP10

[LTC1799CS5#TRPBF]

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SMD5

[LTC6906CS6#TRMPBF]

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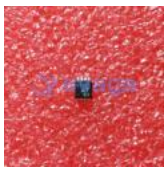
SOT23-5



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