

XC9572 In-System Programmable CPLD

Manufacturers	AMD Xilinx, Inc
Package/Case	PLCC-84
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC9572-10PC84C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC9572-10PC84C is a programmable logic device (PLD) from the Xilinx family of CPLDs (Complex Programmable Logic Devices). It is a low-power, high-performance, 3.3-volt CPLD that contains 72 macrocells and provides up to 1,500 usable gates. The "10" in the part number refers to its maximum operating frequency of 10 MHz, while "PC84C" refers to its package type and pin count.

Features

- 72 macrocells, each with up to 32 product terms and a dedicated flip-flop
- Maximum operating frequency of 10 MHz
- 3.3-volt operation
- Low-power consumption
- In-system programmable (ISP) via the IEEE 1149.1 JTAG interface
- Multiple I/O standards including LVTTTL, LVCMOS, PCI, and LVPECL
- 84-pin plastic leaded chip carrier (PLCC) package

Application

- General digital logic functions
- Glue logic and interface control
- State machine and control logic
- Address decoding and bus interface control
- Data acquisition and control
- Timing and synchronization

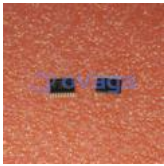


Related Products



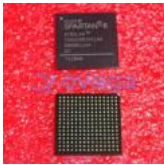
[XC18V01S020C](#)

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SOP-20



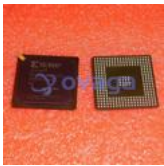
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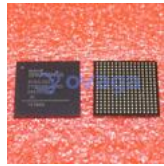
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