

16-Bit, 250 kSPS PulSAR® ADC in MSOP/QFN; Package: MSOP; No of Pins: 10;
Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	MSOP-10
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD7685BRMZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

Power dissipation scales linearly with throughput.

The SPI-compatible serial interface also features the ability, using the SDI input, to daisy chain several ADCs on a single 3-wire bus or provides an optional BUSY indicator. It is compatible with 1.8 V, 2.5 V, 3 V, or 5 V logic using the separate supply VIO.

The AD7685 is housed in a 10-lead MSOP or a 10-lead QFN (LFCSP) with operation specified from -40°C to $+85^{\circ}\text{C}$.

Features

16-bit resolution with no missing codes

Throughput: 250 kSPS

INL: ± 0.6 LSB typical, ± 2 LSB maximum ($\pm 0.003\%$ of FSR)

SINAD: 93.5 dB @ 20 kHz

THD: -110 dB @ 20 kHz

Pseudo differential analog input range 0 V to VREF with VREF up to VDD

No pipeline delay

Single-supply operation 2.3 V to 5.5 V with 1.8 V to 5 V logic interface

Serial interface SPI[®]-/QSPI[™]-/MICROWIRE[™]-/DSP-compatible

Daisy-chain multiple ADCs, BUSY indicator

Power dissipation 1.4 μ W @ 2.5 V/100 SPS 1.35 mW @ 2.5 V/100 kSPS, 4 mW @ 5 V/100 kSPS

Standby current: 1 nA

Application

Battery-powered equipment

Medical instruments

Mobile communications

Personal digital assistants (PDAs)

Data acquisition

Instrumentation

Process controls



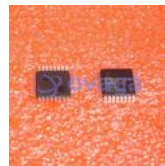
Related Products



[ADAS3022BCPZ](#)
Analog Devices, Inc
LFCSP-40



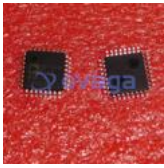
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