

52000 GATE 3.3 VOLT LOGIC CELL ARRAY - NOT RECOMMENDED for NEW DESIGN

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP-160
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC4052XLA-09HQ160I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC4052XLA-09HQ160I is a product of Xilinx, which is a Field-Programmable Gate Array (FPGA) integrated circuit.

Features

- 4000 logic cells
- 25280 bits of block RAM
- 96 input/output (I/O) pins
- Maximum clock frequency of 133 MHz
- 1.8V core voltage and 3.3V I/O voltage

Application

- Digital signal processing
- Embedded processing
- System-level integration
- Communications

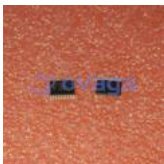


Related Products



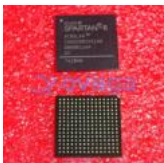
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

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[XC6SLX4-2CSG225C](#)

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BGA-225



[XCF08PV0G48C](#)

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TSOP-48



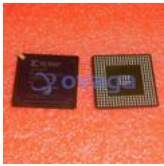
[XC6SLX25-3FTG256C](#)

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BGA-256



[XC6SLX16-3CSG324C](#)

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