

Analogue to Digital Converter, 24 bit, 600 SPS, Single Ended, Serial, Single, 2.7 V

Manufacturers	Analog Devices, Inc
Package/Case	SOIC24
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD7730LBRZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The part features two buffered differential programmable gain analog inputs as well as a differential reference input. The device operates from a single +5 V supply. It accepts four unipolar analog input ranges: 0 mV to +10 mV, +20 mV, +40 mV and +80 mV and four bipolar ranges: ±10 mV, ±20 mV, ±40 mV and ±80 mV. The peak-to-peak resolution achievable directly from the part is 1 in 110,000 counts. An on-chip 6-bit DAC allows the removal of TARE voltages. Clock signals for synchronizing an excitation of the bridge are also provided.

The serial interface on the part can be configured for three-wire operation and is compatible with microcontrollers and digital signal processors. The AD7730L contains self-calibration and system calibration options, and features an offset drift of less than 5 nV/°C and a gain drift of less than 3 ppm/°C. The part is available in a 24-lead SOIC and 24-lead TSSOP package. It is a lower power version of the AD7730 (approximately 1/3 the power).

Features

Resolution of 230,000 Counts (Peak-to-Peak)

Offset Drift: 5 nV/°C

Gain Drift: 2 ppm/°C

Line Frequency Rejection: >150 dB

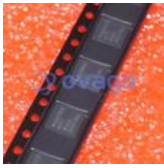
Buffered Differential Inputs

Programmable Filter Cutoffs

Specified for Drift Over Time

Operates with Reference Voltages of 1 V to 5 V

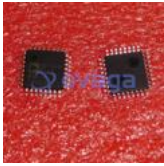
Related Products



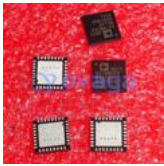
[ADAS3022BCPZ](#)
Analog Devices, Inc
LFCSP-40



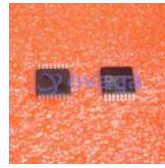
[AD574AJNZ](#)
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PDIP-28



[AD7938BSUZ](#)
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TSSOP-24



[AD9680BCPZ-500](#)
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LFCSP-64