

AD7730BRUZ

Images are for reference only

Data Sheet

Analogue to Digital Converter, 24 bit, 1.2 kSPS, Differential, SPI, Single, 4.75 V

Manufacturers Analog Devices, Inc

Package/Case TSSOP-24

Product Type Data Conversion ICs

RoHS Pb-free Halide free

Please submit RFQ for AD7730BRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

Lifecycle

The part features two buffered differential programmable gain analog inputs as well as a differential reference input. The part operates from a single ± 5 V supply. It accepts four unipolar analog input ranges: 0 mV to ± 10 mV, ± 20 mV, ± 40 mV and ± 80 mV and four bipolar ranges: ± 10 mV, ± 20 mV, ± 40 mV and ± 80 mV. The peak-to-peak resolution achievable directly from the part is 1 in 230,000 counts. An on-chip 6-bit DAC allows the removal of TARE voltages. Clock signals for synchronizing ac excitation of the bridge are also provided.

The serial interface on the part can be configured for three-wire operation and is compatible with microcontrollers and digital signal processors. The AD7730 contains self-calibration and system calibration options, and features an offset drift of less than 5 nV/°C and a gain drift of less than 2 ppm/°C. The part is available in a 24-pin plastic DIP, a 24-lead SOIC and 24-lead TSSOP package.

Features Application

Resolution of 230,000 Counts (Peak-to-Peak) Weigh Scales

Offset Drift: 5 nV/°C Pressure Measurement

Gain Drift: 2 ppm/°C

Line Frequency Rejection: >150 dB

Buffered Differential Inputs

Programmable Filter Cutoffs

Specified for Drift Over Time

Operates with Reference Voltages of 1 V to 5 V

Related Products



ADAS3022BCPZ
Analog Devices, Inc
LFCSP-40



AD574AJNZ
Analog Devices, Inc
PDIP-28



AD7938BSUZ
Analog Devices, Inc
TQFP-32



AD7124-8BCPZ-RL7
Analog Devices, Inc
LFCSP-32



AD7266BSUZ

Analog Devices, Inc
TQPF-32



AD7401YRWZ
Analog Devices, Inc
SOIC-16



Analog Devices, Inc TSSOP-24



AD9680BCPZ-500
Analog Devices, Inc
LFCSP-64