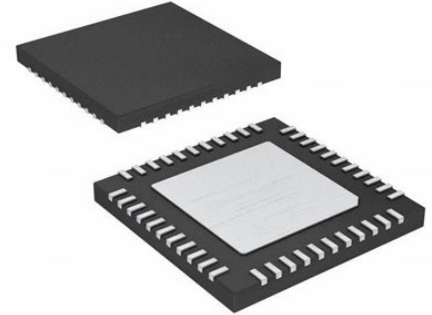


4-Bit 100 Mbps Configurable Dual-Supply Level Translator, Translation - Voltage Levels
4BIT 100 MBPS CONFIG URAB

Manufacturers	ON Semiconductor, LLC
Package/Case	UQFN-12
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for NLSX4014MUTAG or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The NLSX4014 is a 4 bit configurable dual supply bidirectional level translator without a direction control pin. The I/O VCC- and I/O VL ports are designed to track two different power supply rails, VCC and VL respectively. The VCC supply rail is configurable from 1.3 V to 4.5 V while the VL supply rail is configurable from 0.9 V to (VCC - 0.4) V. This allows lower voltage logic signals on the VL side to be translated into higher voltage logic signals on the VCC side, and vice versa. Both I/O ports are auto sensing; thus, no direction pin is required. The Output Enable (EN) input, when Low, disables both I/O ports by putting them in 3 state. This significantly reduces the supply currents from both VCC and VL. The EN signal is designed to track VL.

Features

- Wide HighSide VCC Operating Range: 1.3 V to 4.5 V
- Wide LowSide VL Operating Range: 0.9 V to (VCC - 0.4) V
- HighSpeed with 100 Mb/s Guaranteed Data Rate for VL > 1.6 V
- Low Bit - to - Bit Skew
- Overvoltage Tolerant Enable and I/O Pins
- Non - preferential Powerup Sequencing
- Small packaging: 1.7 mm x 2.0 mm UQFN12

Application

ONSEMI

Related Products



[NLSV2T244MUTAG](#)

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UDFN8



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