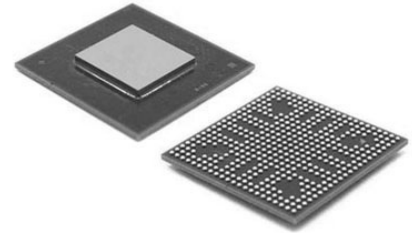


Rail-to-Rail, Very Fast, 2.5 V to 5.5 V, Single-Supply LVDS Comparator in a 12-lead LSCFP Package; Package: LFCSP (3x3mm); No of Pins: 12; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	CSP-12
Product Type	Comparator ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADCMP605BCPZ-WP or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADCMP604/ADCMP605 are very fast comparators fabricated on the Analog Devices, Inc. proprietary XFCB2 process. These comparators are exceptionally versatile and easy to use. Features include an input range from $V_{EE} - 0.5\text{ V}$ to $V_{CCI} + 0.2\text{ V}$, low noise, LVDS-compatible output drivers, and TTL/CMOS latch inputs with adjustable hysteresis and/or shut-down inputs.

The devices offer 1.5 ns propagation delays with 1 ps rms random jitter (RJ). Overdrive and slew rate dispersion are typically less than 50 ps. A flexible power supply scheme allows the devices to operate with a single 2.5 V positive supply and a -0.5 V to $+2.7\text{ V}$ input signal range up to a 5.5 V positive supply with a -0.5 V to $+5.7\text{ V}$ input signal range. Split input/output supplies, with no sequencing restrictions on the ADCMP605, support a wide input signal range with greatly reduced power consumption.

The LVDS-compatible output stage is designed to drive any standard LVDS input. The comparator input stage offers robust protection against large input overdrive, and the outputs do not phase reverse when the valid input signal range is exceeded. High speed latch and programmable hysteresis features are also provided in a unique single-pin control option.

The ADCMP604 is available in a 6-lead SC70 package, and the ADCMP605 is available in a 12-lead LFCSP.

Features

Fully specified rail to rail at>

Input common-mode voltage from -0.2 V to $V_{CCI} + 0.2\text{ V}$

Low glitch LVDS-compatible output stage

1.6 ns propagation delay

37 mW at 2.5 V

Shutdown pin

Single-pin control for programmable hysteresis and latch

Power supply rejection > 60 dB

Application

High speed instrumentation

Clock and data signal restoration

Logic level shifting or translation

Pulse spectroscopy

High speed line receivers

Threshold detection

Peak and zero-crossing detectors

High speed trigger circuitry

Pulse-width modulators

Current-/voltage-controlled oscillators

Automatic test equipment (ATE)

Related Products



[ADCMP573BCPZ](#)

Analog Devices, Inc
QFN



[AD790SQ](#)

Analog Devices, Inc
CDIP-8



[AD9696KR](#)

Analog Devices, Inc
SOP-8



[AD9687BD](#)

Analog Devices, Inc
DIP16



[AD96687BQ](#)

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SOIC-8



[AD790JN](#)

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PDIP-8



[AD9696TQ](#)

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