

LTC4303IMS8#PBF

Data Sheet

IC, HOTSWAP 2WIRE BUS BUFFR, 8MSOP

Manufacturers Analog Devices, Inc

Package/Case MSOP-8

Product Type Interface ICs

RoHS Pb-free Halide free

Lifecycle



Images are for reference only

Please submit RFQ for LTC4303IMS8#PBF or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 hours.

RFO

General Description

Rise-time accelerator circuitry allows the use of larger pull-up resistors while still meeting rise-time requirements. During insertion, the SDA and SCL lines are precharged to 1V to minimize bus disturbances. When driven high, ENABLE allows the LTC4303 to connect after a stop bit or bus idle occurs. Driving ENABLE low breaks the connection between SDAIN and SDAOUT, SCLIN and SCLOUT. READY is an open drain output that indicates when the backplane and card sides are connected together.

Features

Automatic Disconnect of SDA/SCL Lines when Bus is Stuck Low for $\geq 30 \text{ms}$

Recovers Stuck Busses with Automatic Clocking*

Bidirectional Buffer* for SDA and SCL Lines Increases Fanout

Prevents SDA and SCL Corruption During Live Board Insertion and Removal from Backplane

Pin Compatible with LTC4300A-1

Isolates Input SDA and SCL Lines from Output

Compatible with I2CTM, I2C Fast-Mode and SMBus Standards (Up to 400kHz Operation)

READY Open Drain Output

1V Precharge on All SDA and SCL Lines

High Impedance SDA, SCL Pins for>

ENABLE Gates Connection from Input to Output

MSOP 8-Pin and DFN (3mm × 3mm) Packages

Application

Hot Board Insertion

Servers

Capacitance Buffer/Bus Extender

RAID Systems



Related Products



LTC4300A-1IMS8#PBF

Analog Devices, Inc MSOP8



LTC2870IFE#PBF

Analog Devices, Inc TSSOP28



LTC6820HMS#PBF

Analog Devices, Inc MSOP-16



LTC2870IUFD#PBF

Analog Devices, Inc 28-QFN



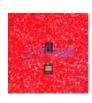
LTC6820IMS#PBF

Analog Devices, Inc MSOP16



LTM2881IV-3#PBF

Analog Devices, Inc LGA32



LTC2854HDD#PBF
Analog Devices, Inc
QFN-10



Analog Devices, Inc
DFN10

LTC2852IDD#PBF