EPM7128ELI84-20

Data Sheet

CPLD MAX 7000 Family 2.5K Gates 128 Macro Cells 62.5MHz CMOS Technology 5V 84Pin PLCC

Manufacturers	Altera Corporation (Intel)
Package/Case	PLCC-84
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Images are for reference only

Please submit RFQ for EPM7128ELI84-20 or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7128ELI84-20 is a programmable logic device (PLD) manufactured by Intel (formerly Altera).

Features

It has a high-density CMOS EEPROM technology with electrically erasable cells

It has 128 macrocells, which can be configured as either logic array blocks (LABs) or embedded memory blocks (EMBs)

It has 84 pins in a plastic quad flat pack (PQFP) package

It operates with a 5V power supply

It has a maximum operating frequency of 125 MHz

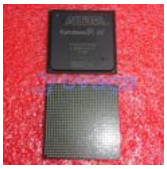
It has a 20 ns maximum propagation delay time

Application

EPM7128ELI84-20 is commonly used in digital systems design, such as in telecommunications, networking, and industrial control applications.

It can be used for various functions such as signal processing, arithmetic processing, and control functions.

Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N](#)

Altera Corporation (Intel)

BGA-100

[EPM1270T144A5N]

Altera Corporation (Intel)

TQFP-144

[EPM570F256C5N]

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FBGA-256

[EP2C35F672C8N]

Altera Corporation (Intel)

FBGA-672

[EPM7128AETC100-10]

Altera Corporation (Intel)

TQFP-100

[EP2C35F484C7N]

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FBGA-484

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Altera Corporation (Intel)

FBGA-484