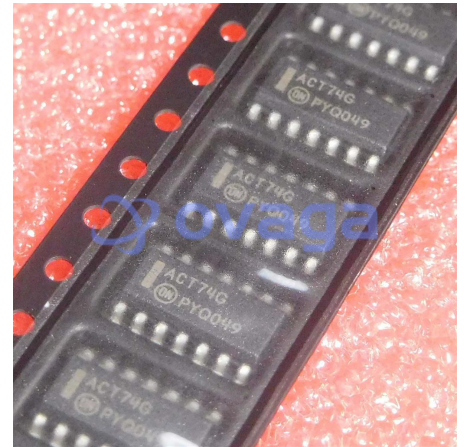


Dual D-Type Positive Edge-Triggered Flip-Flop, Flip Flops 5V CMOS Dual D-Type Positive Edge

Manufacturers	<a href="#">ON Semiconductor, LLC</a>
Package/Case	SOIC-14
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for MC74ACT74DR2G or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The MC74AC74/74ACT74 is a dual D-type flip-flop with Asynchronous Clear and Set inputs and complementary (Q, Q) outputs. Information at the input is transferred to the outputs on the positive edge of the clock pulse. Clock triggering occurs at a voltage level of the clock pulse and is not directly related to the transition time of the positive-going pulse. After the Clock Pulse input threshold voltage has been passed, the Data input is locked out and information present will not be transferred to the outputs until the next rising edge of the Clock Pulse input. Asynchronous Inputs: LOW input to SD (Set) sets Q to HIGH level LOW input to CD (Clear) sets Q to LOW level Clear and Set are independent of clock Simultaneous LOW on CD and SD makes both Q and Q HIGH

## Features

Outputs Source/Sink 24 mA

ACT74 Has TTL Compatible Inputs CD1SD2 D1Q1CP2Q2

Pb-Free Packages Are Available

## Application

ONSEMI



## Related Products



### [MC14094BDR2G](#)

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SOIC-16



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### [MC74VHC1G32DFT1G](#)

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SC-70



### [MC74LCX16245DTG](#)

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TSSOP-48



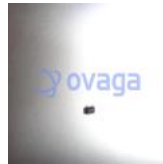
### [MC14013BDR2G](#)

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