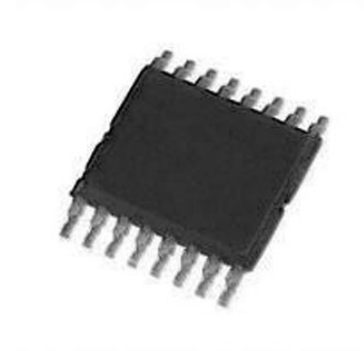


23 Max On Resistance, 【15 V/12 V/ 【5 V iCMOS® Quad SPST Switch

|               |                                     |
|---------------|-------------------------------------|
| Manufacturers | <a href="#">Analog Devices, Inc</a> |
| Package/Case  | TSSOP-16                            |
| Product Type  | Analog Switch ICs                   |
| RoHS          | Rohs                                |
| Lifecycle     |                                     |



Images are for reference only

Please submit RFQ for ADG1412YRUZ-REEL7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADG1412L is a monolithic complementary metal-oxide semiconductor (CMOS) device containing four independently selectable switches designed on an iCMOS® process. Industrial CMOS (iCMOS) is a modular manufacturing process combining high voltage CMOS and bipolar technologies.

The on-resistance profile is flat over the full analog input range, ensuring excellent linearity and low distortion (1.5 Ω typical) when switching signals.

The ADG1412L contains four independent SPST switches, and these switches are turned on with Logic 1. Each switch conducts equally well in both directions when on and has an input signal range that extends to the supplies. In the off condition, signal levels up to the supplies are blocked.

An external  $V_L$  supply provides flexibility for lower logic control. The ADG1412L is both 1.2 V and 1.8 V JEDEC standard compliant.

## PRODUCT HIGHLIGHTS

### APPLICATIONS

2.6 Ω maximum on resistance over temperature.

Minimum distortion.

$V_L$  supply for low logic-level compatibility.

JEDEC standard compliant for both 1.2 V and 1.8 V logic levels.

Guaranteed switch off when digital inputs are floating.

24-lead, 4 mm × 4 mm LFCSP.

## Features

1.5  $\Omega$  on resistance for  $\pm 15$  V dual supply at 25°C

0.3  $\Omega$  on-resistance flatness for  $\pm 15$  V dual supply at 25°C

0.1  $\Omega$  on-resistance match between channels for  $\pm 15$  V dual supply at 25°C

Fully specified at  $\pm 15$  V, +12 V,  $\pm 5$  V

5 V to 16.5 V single-supply operation

VL supply for low logic-level compatibility

1.8 V JEDEC standard compliant (JESD8-7A)

1.2 V JEDEC standard compliant (JESD8-12A.01)

Rail-to-rail operation

24-lead, 4 mm  $\times$  4 mm LFCSP

## Application

Automated test equipment

Data-acquisition systems

Battery-powered systems

Sample-and-hold systems

FPGA and microcontroller systems

Audio signal routing

Video signal routing

Communications systems

Relay replacement

## Related Products



### [ADV7181CBSTZ](#)

Analog Devices, Inc  
LQFP-64



### [AD724JR](#)

Analog Devices, Inc  
SOIC-16



### [ADV7391WBSPZ](#)

Analog Devices, Inc  
LFSCP-3



### [ADV7341BSTZ](#)

Analog Devices, Inc  
LQFP-64



### [AD8170AR](#)

Analog Devices, Inc  
SOP8



### [ADV7393BCPZ](#)

Analog Devices, Inc  
LFCSP-VQ-40



### [ADV7390BCPZ](#)

Analog Devices, Inc  
QFN32



### [ADUM4160BRIZ](#)

Analog Devices, Inc  
SOIC-16