

64 Macrocell CPLD with Enhanced Clocking

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	PLCC44
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCR5064C-10PC44C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

XCR5064C-10PC44C is a type of Field-Programmable Gate Array (FPGA) manufactured by Xilinx Inc.

### Features

It has a maximum of 5064 logic cells.

It operates on a 3.3V power supply.

It has a maximum of 192 user I/O pins.

It has 36 dedicated input pins for clock signals.

It has 10 ns maximum pin-to-pin delay.

### Application

XCR5064C-10PC44C can be used in a variety of applications that require high-speed processing, including digital signal processing, telecommunications, and multimedia.

It can also be used in industrial control and automation, aerospace, and defense applications.



## Related Products



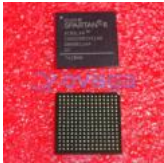
### [XCF18V01S020C](#)

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SOP-20



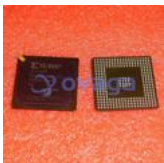
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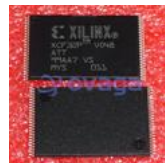
### [XC6SLX25-3FTG256C](#)

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