

Logic IC; Function: 3-to-8 line Decoder/Demultiplexer

| | |
|---------------|---|
| Manufacturers | Renesas Technology Corp |
| Package/Case | DIP-16 |
| Product Type | Logic ICs |
| RoHS | |
| Lifecycle | |



Images are for reference only

Please submit RFQ for HD74LS138P or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

HD74LS138P is a 3-to-8 line decoder/demultiplexer integrated circuit (IC) made by Hitachi, which is now a part of Renesas Electronics Corporation. It belongs to the 74LS series of TTL (transistor-transistor logic) logic ICs.

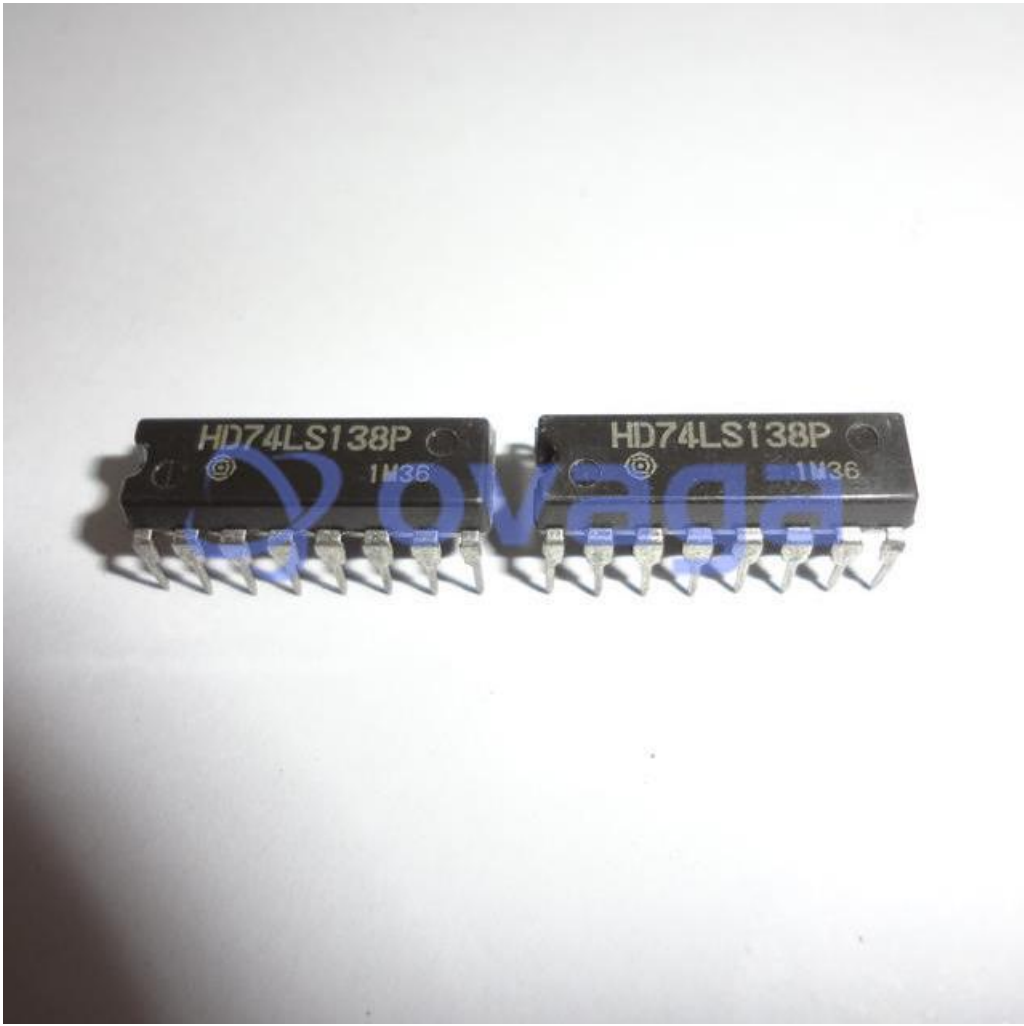
Features

- Provides 3-to-8 decoding or demultiplexing functions
- Active-low outputs
- Schottky-clamped for improved noise margin
- TTL-compatible inputs
- Low power consumption
- Operating temperature range: -40°C to 85°C
- 16-pin DIP (dual in-line package) package

Application

- Address decoding in microprocessor systems
- Memory address decoding
- Programmable logic devices
- General-purpose logic circuits





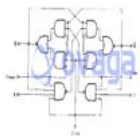
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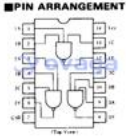
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